


The diagram shows a 3V3_PYLK signal line connected to the common cathode of three LEDs (RED, GREEN, BLUE). Each LED is connected to the signal line through a 680R resistor (R4, R5, and R8 respectively). The LEDs are labeled LED1, LED2, and LED3. The resistors are labeled R4, R5, and R8. The LEDs are labeled LED RED, LED GREEN, and LED BLUE.

The image displays three circuit diagrams for different voltage regulators:

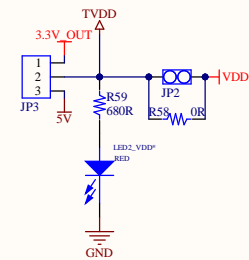
- LM1117-3.3:** This diagram shows the regulator (U9) with its input (Vin) connected to a 5V_USB_CHGR source through a 10uF capacitor (C3). The output (Vout) is connected to a 3V3_PYLK output through a 100nF capacitor (C4). The regulator is grounded (GND).
- MT19700:** This diagram shows the regulator (U13) with its input (VIN) connected to a 3V3_PYLK source through a 5.1K resistor (R23). The output (OUT) is connected to a 3V3_USB PYLK output through a 16K resistor (R24) and a 10uF capacitor (C6). The regulator is grounded (GND).
- MT19700:** This diagram shows the regulator (U16) with its input (VIN) connected to a 5V_USB_CHGR source through a 5.1K resistor (R28). The output (OUT) is connected to a 5V_USB PYLK output through a 16K resistor (R29) and a 10uF capacitor (C15). The regulator is grounded (GND).

Pin 1 to 5 connections for the STM32F103C8T6 microcontroller:

- Pin 1: PYLK_BOOT0
- Pin 2: SWD_SDA, 3V3_PYLK
- Pin 3: SWD_SDA
- Pin 4: SWD_SCK
- Pin 5: SWD_SCK

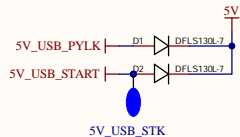
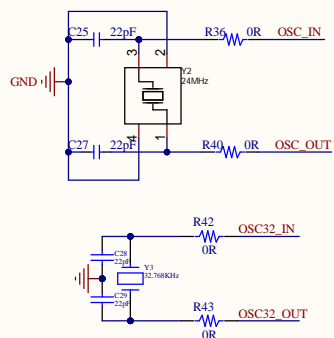
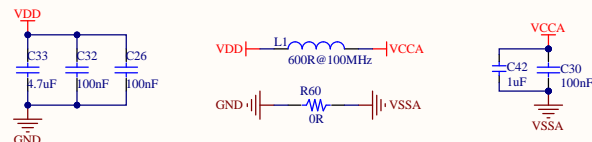
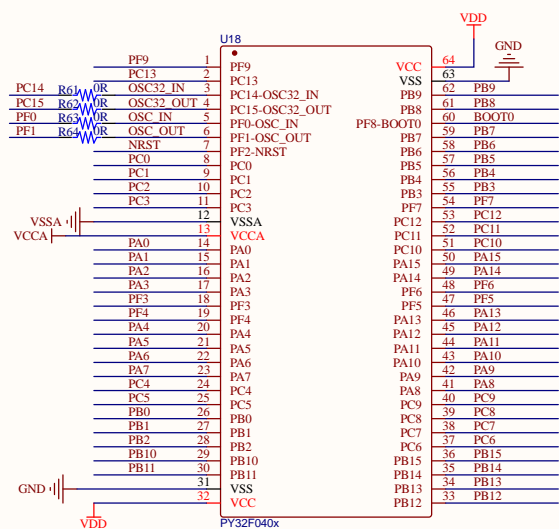
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Sheet: 1 of 2	Revision: *	

The diagram illustrates the VDD Selection circuit. It features a JP3 header with pins 1, 2, and 3. Pin 1 is connected to 3.3V_OUT, and pin 2 is connected to 5V. The 3.3V_OUT pin is also connected to a TVDD pin. A 680R resistor (R59) is connected between TVDD and a node. This node is connected to a 5V pin on a JP2 header. A 0R resistor (R58) is connected between the 5V pin on JP2 and a VDD pin. A LED_VDDP pin is connected to the VDD pin and a GND pin.



5V_PWR_Selection

The diagram illustrates a 5V_PWR_Selection circuit. It features two input signals, 5V_USB_PYLK and 5V_USB_START, which are connected to a 2-to-1 multiplexer (MUX) labeled D1 and D2. The MUX output is connected to a 5V_USB_STK signal. The MUX is controlled by a 5V signal. The output of the MUX is connected to a 5V_USB_STK signal.

[illegible]

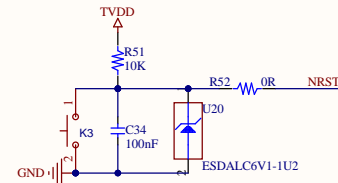
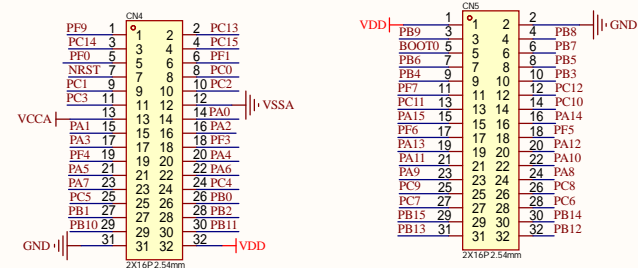
Extension PIN

ZL16P2 54mm

Pin	Function
1	PF9
2	PC13
3	PC14
4	PC15
5	PF0
6	PF1
7	NRST
8	PC0
9	PC1
10	PC2
11	PC3
12	PA4
13	PA5
14	PA6
15	PA7
16	PA8
17	PA9
18	PA10
19	PA11
20	PA12
21	PA13
22	PA14
23	PA15
24	PA16
25	PA17
26	PA18
27	PA19
28	PA20
29	PA21
30	PA22
31	PA23
32	PA24

ZL16P2 54mm

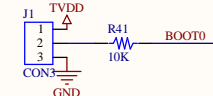
Pin	Function
1	PB9
2	BOOT0
3	PB6
4	PB7
5	PB4
6	PF7
7	PC11
8	PA15
9	PF6
10	PA13
11	PA11
12	PA9
13	PC9
14	PC7
15	PB15
16	PB13
17	GND
18	PB8
19	PB5
20	PB7
21	PB3
22	PC2
23	PC10
24	PA14
25	PC3
26	PA10
27	PA12
28	PA18
29	PA20
30	PA22
31	PA24
32	PA26



LED



BOOT_SEL



USB & POWER

The diagram illustrates the USB and power connections for a microcontroller (U21). The microcontroller is shown with its pins and internal components. The connections are as follows:

- Power Connections:**
 - VBUS:** Connected to the 5V_USB_START signal through a 5.1k resistor (R55) to pin A4 and to pin B9.
 - SUB1:** Connected to the 5V_USB_START signal through a 5.1k resistor (R56) to pin A9 and to pin B4.
 - VBUS:** Connected to the 5V_USB_START signal through a 5.1k resistor (R57) to pin B1.
- Signal Connections:**
 - DP1:** Connected to pin A7.
 - DN1:** Connected to pin A8.
 - CC1:** Connected to pin A5.
 - CC2:** Connected to pin B5.
 - DP2:** Connected to pin B6.
 - DN2:** Connected to pin B7.
 - SUB2:** Connected to pin B8.
- Ground Connections:**
 - GND:** Connected to pins A1, A12, B12, B8, B7, B6, B5, B4, B1, and B2.
 - PA11:** Connected to pin A11.
 - PA12:** Connected to pin A12.
- Internal Components:**
 - Capacitors:** C41 (4.7nF) and C42 (100nF) are connected to the VBUS pins (A4, B9, B1) to ground.
 - Resistors:** R55 (5.1k), R56 (5.1k), and R57 (5.1k) are used for signal conditioning.
 - Diode:** A diode (KH-TYPE-C-16P) is connected between the VBUS pins (A4, B9, B1) and ground.

